

# AIC

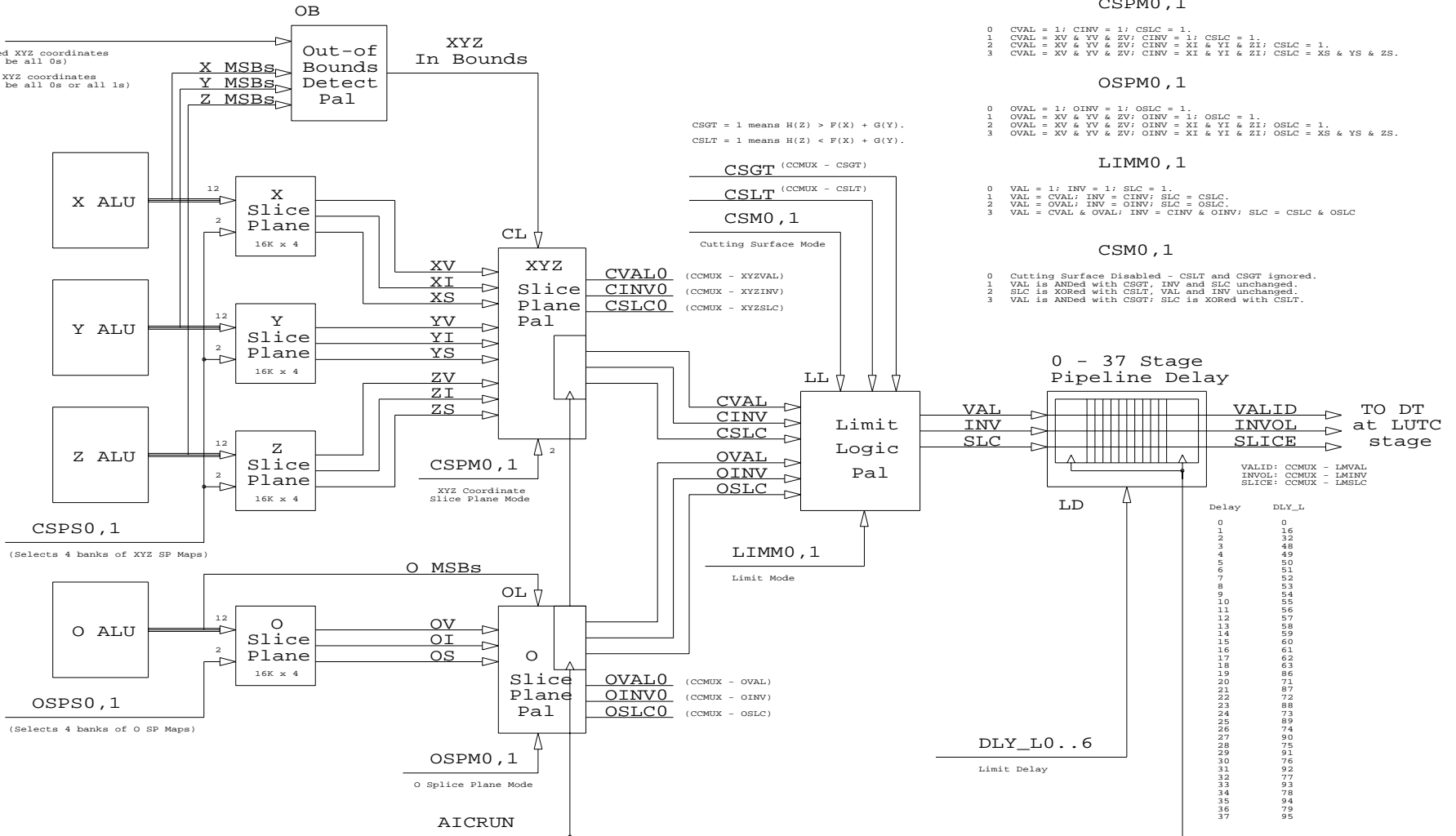
## ORM FRACTION BITS

AIC  
0..31

Dynamic Digital Displays 130 Radnor-Chester Road St. Davids, PA 19087		
Title	CT Coordinate Generator	
Size	Document Number	REV
C	1000CTB001	00
Date:	February 9, 1992	Sheet 1 of 1

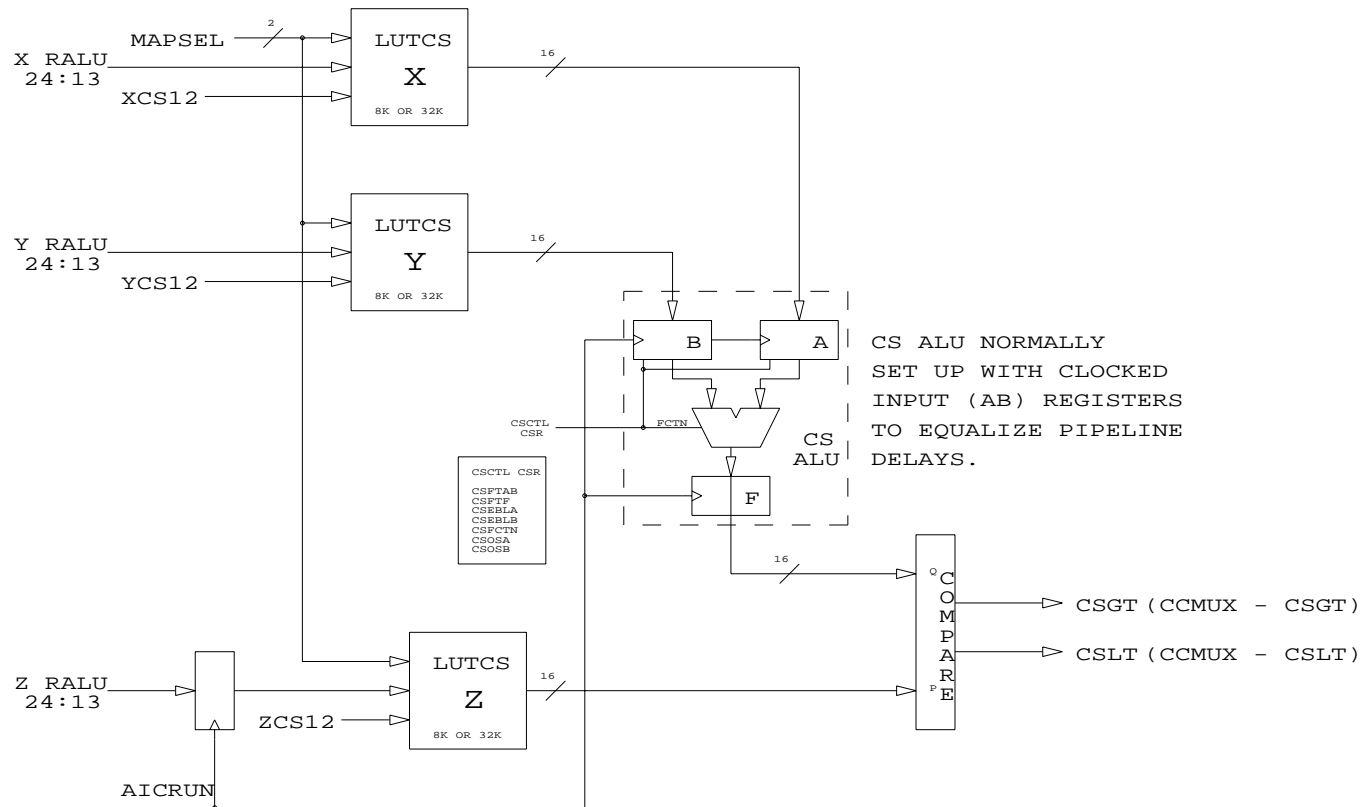
SGNCRD

0 for unsigned XYZ coordinates  
(MSBs must be all 0s)  
1 for signed XYZ coordinates  
(MSBs must be all 0s or all 1s)

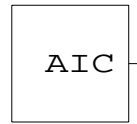
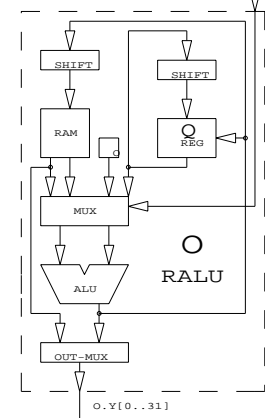
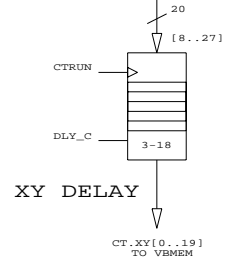
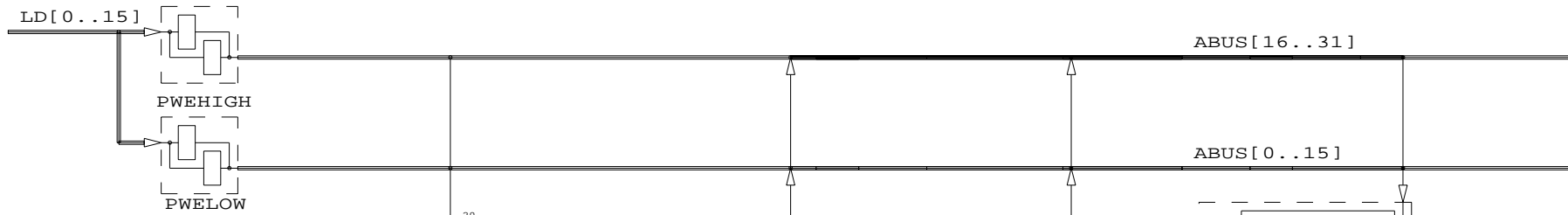


## VS-III CT Limit Logic

	MAPSEL	XCS12	YCS12	ZCS12
8K MAPs	NA	X25 XOR CSSEL1	Y25 XOR CSSEL1	A25 XOR CSSEL1
32K MAPs	CSSEL1:0	X25	Y25	A25



## CUTTING SURFACE LOGIC



**ORM AND BA CONTROL**

OUTPUT BUS OF O ALU (ORM ALU A)

MA2SRC	MA1SRC	BASRC	IDLE	LDADDR	LDOMEM	BOTH
0	0	0	A BUS	O ALU	O ALU	O ALU
0	0	1	A BUS	A BUS	O ALU	O ALU
0	1	0	A BUS	O ALU	A BUS	O ALU
0	1	1	A BUS	A BUS	A BUS	A BUS
1	0	0	O ALU	O ALU	O ALU	O ALU
1	0	1	O ALU	A BUS	O ALU	O ALU
1	1	0	O ALU	O ALU	A BUS	O ALU
1	1	1	O ALU	A BUS	A BUS	A BUS

MA2SRC	ORM ALU B
0	AIC
1	A BUS

